

Proposed subclause 999.4 for 100G BiDi

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Clauses 140 and 160

- 140.4 defines PMD MDIO function mapping of 100GBASE-DR/FR1/LR1
- 160.4 from 802.3cp is another reference of this specification, which is for 50G BiDi
- It is proposed to reuse these subclauses for 100G BiDi
- Following slides show content reuse and suggested minor changes
 - Black text: reused content from 140/160
 - Blue text: difference between 140 and 160
 - Red text: notes and discussion point

Proposed subclause 999.4 (references: subclause 140.4 and 160.4)

999.4 PMD MDIO function mapping

The optional MDIO capability described in Clause 45 defines several variables that may provide control and status information for and about the PMD. If the MDIO interface is implemented, the mapping of MDIO control variables to PMD control variables shall be as shown in Table 999–2 and the mapping of MDIO status variables to PMD status variables shall be as shown in Table 999–3. (Exact copy of subclauses 140.4 and 160.4)

Table 999–2—MDIO/PMD control variable mapping

MDIO control variable	PMA/PMD register name	Register/bit number	PMD control variable
Reset	PMA/PMD control 1 register	1.0.15	PMD_reset
Global PMD transmit disable	PMD transmit disable register	1.9.0	PMD_global_transmit_disable

Table 999–3—MDIO/PMD status variable mapping

MDIO status variable	PMA/PMD register name	Register/bit number	PMD status variable
Fault	PMA/PMD status 1 register	1.1.7	PMD_fault
Transmit fault	PMA/PMD status 2 register	1.8.11	PMD_transmit_fault
Receive fault	PMA/PMD status 2 register	1.8.10	PMD_receive_fault
Global PMD receive signal detect	PMD receive signal detect register	1.10.0	PMD_global_transmit_detect

Recap: Table 999-2 and 999-3 have been adopted in November meeting Motion#3

Thank you

Any questions?