Cl **FM** *SC* **FM** *P* **8** *L* **12** # 166

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

need to update officers and Editors

SuggestedRemedy

Replace "Jon Lewis" with "Natalie Wienckowski" for TF Chair.
Replace "Natalie Wienckowski" with "Steve Gorshe" for TF Vice-Chair.
Add "Valarie Maguire" as Clause Editor.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **FM** *SC* **FM** *P* **10** *L* **32** # 167

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

add "dm"

SuggestedRemedy

Change: IEEE Std 802.3xx-20xx
To: IEEE Std 802.3dm-202x

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **FM** *SC* **FM** *P* **13** *L* **1** # 7

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting, Microchip, an

Comment Type **E** *Comment Status* **D** *EZ*

Insert Amendments 10 through 12

SuggestedRemedy

Insert:

IEEE Std 802.3da™-2026
Amendment 10—This amendment includes changes to IEEE Std 802.3-2022 and adds Clause 188 through Clause 189. This amendment adds Physical Layer specifications and management parameters for enhancement of multidrop 10 Mb/s operation based on the 10BASE-T1S PHY specified in Clause 147 of IEEE Std 802.3-2022, and specifies optional provision of power over single balanced pair mixing segments. Additionally, this amendment includes additions and changes to Clause 148 to automatically allocate node IDs (Dynamic PLCA).

IEEE Std 802.3dk™-20xx
Amendment 11—This amendment includes changes to IEEE Std 802.3-2022 and adds Clause 168. This amendment adds Physical Layer specifications and management parameters for 100 Gb/s Ethernet optical interfaces for bidirectional operation over a single strand of single-mode fiber.

IEEE Std 802.3dg™-20xx
Amendment 12—This amendment to IEEE Std 802.3-2022 specifies additions and appropriate modifications to add 100 Mb/s Physical Layer (PHY) specifications and management parameters for operation, and associated optional provision of power, over a single balanced pair of conductors.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **1** *SC* **1.4** *P* **30** *L* **35** # 2

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting, Microchip, an

Comment Type **E** *Comment Status* **D** *EZ*

Missing Abbreviatons for ACT and TDD

SuggestedRemedy

Insert:

ACT Asymmetric Coded Transceiver
TDD Time Division Duplex

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Insert:

ACT Asymmetric Concurrent Transmission
TDD Time Division Duplex

Cl **1** *SC* **1.4.88** *P* **30** *L* **20** # 322

Shen, David Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

SuggestedRemedy

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with 100 Mb/s in the reverse direction).

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

To: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with 100 Mb/s in the reverse direction).

Cl **30** *SC* **30.3.2.1.2** *P* **31** *L* **27** # 20

Sun, Jingcong Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

change '100M+2.5GBASE-T1/V1' to '100M+5GBASE-T1/V1'

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **30** *SC* **30.3.2.1.2** *P* **31** *L* **29** # 21

Sun, Jingcong Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

change '100M+2.5GBASE-T1/V1' to '100M+5GBASE-T1/V1'

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **30** *SC* **30.3.2.1.2** *P* **31** *L* **38** # 22

Sun, Jingcong Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

change '100M+2.5GBASE-T1/V1' to '100M+10GBASE-T1/V1'

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **30** *SC* **30.3.2.1.2** *P* **31** *L* **40** # 23

Sun, Jingcong Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

change '100M+2.5GBASE-T1/V1' to '100M+10GBASE-T1/V1'

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **30** *SC* **30.3.2.1.3** *P* **32** *L* **6** # 24

Sun, Jingcong Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

change '100M+2.5GBASE-T1/V1' to '100M+5GBASE-T1/V1'

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **30** *SC* **30.3.2.1.3** *P* **32** *L* **8** # 25

Sun, Jingcong Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

change '100M+2.5GBASE-T1/V1' to '100M+5GBASE-T1/V1'

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **30** *SC* **30.3.2.1.3** *P* **32** *L* **15** # 26

Sun, Jingcong Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

change '100M+2.5GBASE-T1/V1' to '100M+10GBASE-T1/V1'

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **30** *SC* **30.3.2.1.3** *P* **32** *L* **17** # 27

Sun, Jingcong Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

change '100M+2.5GBASE-T1/V1' to '100M+10GBASE-T1/V1'

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **30** *SC* **30.5.1.1.2** *P* **32** *L* **33** # 168

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

Replace references to Clause 200 with references to Clause 201 and Clause 202.

SuggestedRemedy

Duplicate the groups in 30.5.1.1.2 to include reference to Clause 201 and Clause 202 and remove the reference to Clause 200, similar to what was done for 30.3.2.1.3.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **30** *SC* **30.5.1.1.2** *P* **32** *L* **33** # 160

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

Eliminate links to Clause 200

SuggestedRemedy

Make copies of all tiems and create links to Clause 201 and to Clause 202.

Proposed Response *Response Status* **Z**

PROPOSED REJECT.

This comment was WITHDRAWN by the commenter.

Duplicate of #168

Cl **30** *SC* **30.6.1.1** *P* **33** *L* **15** # 161

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

Delete -V1 rows as Clause 98 Auto-Negotiation doesn't apply to -V1.

SuggestedRemedy

Delete rows in lines 15, 16, 25, 26, 35, and 36.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **45** *SC* **45.2.1.214** *P* **39** *L* **4** # 6

Maguire, Valerie — Copperopolis; aff'l w/ CME Consulting, Microchip, an

Comment Type **E** *Comment Status* **D** *EZ*

IEEE P802.3da has published.

SuggestedRemedy

On P39, L4:
Replace, " IEEE Std 802.3da-202x" with, " IEEE Std 802.3da-2026

On P39, L40:
Replace, " IEEE Std 802.3da-202x" with, " IEEE Std 802.3da-2026

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **46** *SC* **46.1** *P* **40** *L* **11** # 323

Shen, David — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

SuggestedRemedy

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with 100 Mb/s in the reverse direction).

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

To: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with 100 Mb/s in the reverse direction).

Cl **46** *SC* **46.1** *P* **40** *L* **19** # 324

Shen, David — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

SuggestedRemedy

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with 100 Mb/s in the reverse direction).

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

To: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with 100 Mb/s in the reverse direction).

Cl **46** *SC* **46.1.1** *P* **40** *L* **28** # 325

Shen, David — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

SuggestedRemedy

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with 100 Mb/s in the reverse direction).

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

To: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with 100 Mb/s in the reverse direction).

Cl **46** *SC* **46.1.3** *P* **40** *L* **42** # 326

Shen, David Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

SuggestedRemedy

Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation(including asymmetric PHYs with 100 Mb/s in the reverse direction).

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with one of these rates in one direction and 100 Mb/s in the reverse direction).

To: Physical Coding Sublayer (PCS) for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s operation (including asymmetric PHYs with 100 Mb/s in the reverse direction).

Cl **46** *SC* **46.3.2.1** *P* **41** *L* **18** # 327

Shen, David Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The frequency of RX_CLK may be derived from the received data or it may correspond to be that of a nominal clock (e.g., TX_CLK)

SuggestedRemedy

The frequency of RX_CLK may be derived from the received data or the nominal clock (e.g., TX_CLK)

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **46** *SC* **46.6.2.3** *P* **41** *L* **40** # 169

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

Row with "…" is not needed as there is no row below the one being added.

SuggestedRemedy

Delete last row of Table in 46.6.2.3.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **200** *SC* **200.1.1** *P* **46** *L* **18** # 170

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

In list under "where", put a comma after the first two items and a period after the last. Also on P73/L50 and P156/L18.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **200** *SC* **200.12** *P* **63** *L* **50** # 28

Sun, Jingcong Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

change '100Mb/s' to '100 Mb/s'

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.1.1** *P* **72** *L* **26** # 158

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

The Editor's note is no longer needed

SuggestedRemedy

Delete boxed Editor's note

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.1.1** *P* **73** *L* # 29

Tan, Yuxuan Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

The direction of arrows in LS_PATH in Figure 201-1 should be reversed.

SuggestedRemedy

Change direction of arrows in LS_PATH in Figure 201-1 to LS_RX PCS <- LS_RX PMA <---------- LS_TX PMA <- LS_TX PCS.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.1.1** *P* **73** *L* **17** # 145

Pandey, Sujan Velinktech

Comment Type **T** *Comment Status* **D** *EZ*

Figure 201-1, the arrows of LS_PATH is not correct

SuggestedRemedy

All arrows of LS_PATH need to be reversed

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.1.3** *P* **74** *L* **48** # 171

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

Change: block diagram of PHY_D device.
To: block diagram of the PHY_D device.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.1.3.2** *P* **78** *L* **45** # 172

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

The DME definition was moved

SuggestedRemedy

Change: 201.4.2.3
To: 201.5.2.3.1, or appropriate reference is comment combining the LS and HS PMA transmit functions is accepted.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.1.5** *P* **80** *L* **15** # 34

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The statement "The LS_TX PCS generates a continuous stream of DME symbols that are transmitted via the LS_TX PMA" is not correct

SuggestedRemedy

Change to "The LS_TX PCS generates a continuous stream of bits that are transmitted via the LS_TX PMA"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Added Clause 201.1.5

Cl **201** *SC* **201.2** *P* **80** *L* **49** # 173

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

SuggestedRemedy

Delete Editor's note

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.2.1.1** *P* **81** *L* **17** # 174

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

98.4.2 isn't in the spec

SuggestedRemedy

Change "98.4.2" to "External" character type.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.2.1.2.2** *P* **82** *L* **4** # 175

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

The Link Monitor state diagram is in Clause 201

SuggestedRemedy

Change: External reference to Figure 149-33
To: Internal reference to Figure 201-23-Link Monitor state diagram.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.2.1.2.2** *P* **82** *L* **64** # 309

Razavi, Alireza — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Wrong figure reference: Figure 149-33 should be replaced by Figure 201-23.

SuggestedRemedy

See comment.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.2.2.3.1** *P* **86** *L* **17** # 35

Jonsson, Ragnar — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

PAM2 is also used in the training frames

SuggestedRemedy

Add at the end of line 17: ", and HS_PATH training frames"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.3.2.2** *P* **91** *L* **45** # 36

Jonsson, Ragnar — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Incorrect figure reference to Figure 201–6

SuggestedRemedy

Replace Figure 201–6 with Figure 201–7

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.3.2.3** *P* **92** *L* **64** # 310

Razavi, Alireza — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Reference to EEE: Figure 149-18 should be moved to Clause 201, and the EEE-related item should be removed from it.

SuggestedRemedy

See comment.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.3.2.3** *P* **93** *L* **47** # 177

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

SuggestedRemedy

Change: fifty To: 50

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.3.2.3** *P* **93** *L* **48** # 155

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

no EEE

SuggestedRemedy

Remove reference to Figure 149-19

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.3.2.3** *P* **93** *L* **49** # 178

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

Figure 149-19 is for EEE only.

SuggestedRemedy

Remove: and Figure 149-19

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.3.2.3** *P* **93** *L* **64** # 311

Razavi, Alireza Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Reference to EEE: Figure 149-19 should be moved to Clause 201; EEE will be removed from it.

SuggestedRemedy

See comment.

Proposed Response *Response Status* **W**

PROPOSED REJECT.

Added subclause 201.3.2.3

Figure 149-19 is for EEE only, so there is nothing left when this is removed.

Cl **201** *SC* **201.4** *P* **96** *L* **3** # 38

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

With the current structure of the document, it would make sense to have Figures 201-10 closer to Figure 201-6

SuggestedRemedy

Move figure 201-10 forward in the document to be closer to Figure 201-6

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Move Figure 201-10 to 201.3.2 where it is referenced.

Cl **201** *SC* **201.4.2.2** *P* **97** *L* **2** # 40

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The text "The symbol period, T, is 1000 / 117.1875 ns." is confusing

SuggestedRemedy

Replace with either "The symbol period, T, is (1 /117.1875)us." or "The symbol period, T, is (1 /117.1875MHz). "

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change: The symbol period, T, is 1000 / 117.1875 ns.

To: The symbol period, T, is (1000 / 117.1875) ns.

Cl **201** *SC* **201.4.2.2** *P* **97** *L* **16** # 43

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Incorrect clause reference to 201.5.2.7.

SuggestedRemedy

Change reference to 201.2.6.4

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2** *P* **97** *L* **20** # 44

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The word "the" should be removed from "During transmission, the four blocks"

SuggestedRemedy

Change to "During transmission, four blocks"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Changed subclause to 201.4.2.2.

Cl **201** *SC* **201.4.2.2** *P* **97** *L* **35** # 209

van Dyck, Peter Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Figure 201-11: The output of the MUX has the wrong name for data transmitted to PMA

SuggestedRemedy

Replace "symb_tx" with "tx_symb"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.1** *P* **97** *L* **48** # 47

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The phrase "and vice versa" is ambiguous and probably wrong.

SuggestedRemedy

Remove the text "and vice versa"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Changed subclause to 201.4.2.2.1.

Cl **201** *SC* **201.4.2.2.2** *P* **98** *L* **10** # 179

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

SuggestedRemedy

Remove period by itself on line 10.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.2** *P* **98** *L* **30** # 117

Lo, William Axonne Inc.

Comment Type **T** *Comment Status* **D** *EZ*

Missing Line with arrow from RS_FEC to 24-bit Parity

SuggestedRemedy

Draw the missing line

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.2** *P* **98** *L* **31** # 50

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Missing connection from "RS-FEC encoder" block to "24-bit Parity" block

SuggestedRemedy

Add missing connection

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.2** *P* **99** *L* **27** # 51

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Arrow from "Descrambler" to "adder" is in the wrong direction.

SuggestedRemedy

Revers the direction of the arrow from "Descrambler" to "adder"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.2** *P* **99** *L* **28** # 147

Pandey, Sujan Velinktech

Comment Type **T** *Comment Status* **D** *EZ*

Arrow of Descrambler is not correct

SuggestedRemedy

arrow needs to be reversed

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.3** *P* **99** *L* **36** # 52

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Scope of the sentence "For values shown as binary, the leftmost bit is the first transmitted bit" is not clear.

SuggestedRemedy

Add the text "For Figure 201-12," in front of "For values shown as binary, the leftmost bit is the first transmitted bit"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.3** *P* **99** *L* **38** # 53

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Not desirable to start a new paragraph (or a sentence) with digits. It also implies that 64B/65B is an encoding without explicitly stating so (64B/65B is also sometimes used to describe the block).

SuggestedRemedy

Change "64B/65B encodes" to "The 64B/65B encoding, encodes"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.3** *P* **99** *L* **44** # 55

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

In accurate to use the text "eight characters"

SuggestedRemedy

Change "eight characters" to "eight octets".

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.3** *P* **99** *L* **51** # 135

van Dyck, Peter Infineon

Comment Type **E** *Comment Status* **D** *EZ*

tx_coded and rx_coded are incorrectly defined in the text.

SuggestedRemedy

Replace "tx_coded<31:0>" with "tx_coded<64:0>"
Replace "rx_coded<31:0>" with "rx_coded<64:0>"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.5** *P* **100** *L* **8** # 56

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The text incorrectly states that "The XGMII encodes a control character into an octet" which is not correct. This text is unnecessary and is probably best removed.

SuggestedRemedy

Remove the sentence "The XGMII encodes a control character into an octet (an eightbit value)."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.5** *P* **100** *L* **8** # 57

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The paragraph starting on line 8 probably does not bring any value to the document.

SuggestedRemedy

Remove the paragraph starting on line 8.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.14** *P* **101** *L* **15** # 62

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The statement "The group of 300 bits are encoded using a Reed-Solomon encoder" is not correct. There are 276 bits encoded, and 24-bit parity, resulting in a total of 300 bits.

SuggestedRemedy

Change "The group of 300 bits are encoded using a Reed-Solomon encoder" to "The data frame is encoded using a Reed-Solomon encoder"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.14** *P* **101** *L* **18** # 63

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The text "For the purposes of this clause" looks like an unnecessary qualification of the statement.

SuggestedRemedy

Remove the text "For the purposes of this clause"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.14** *P* **101** *L* **19** # 137

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting, Microchip, an

Comment Type **E** *Comment Status* **D** *EZ*

The third term in some Reed-Solomon codes is stated incorrectly.

SuggestedRemedy

P101, L19 - Replace, "RS-FEC(50,46,2^6)" with "RS-FEC(50,46,6)"

P160, L2 - Replace, "RS-FEC(130,124,2^8)" with "RS-FEC(130,124,8)"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.14** *P* **101** *L* **53** # 64

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The paragraph starting in line 53 is redundant and has better description at line 7 on page 102

SuggestedRemedy

Remove the paragraph starting on line 53

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.15** *P* **102** *L* **51** # 65

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The LR_PATH does not really have superframe, so better to use the word "frame".

SuggestedRemedy

Replace "superframe" with "frame"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.2.1** *P* **97** *L* **54** # 49

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Incorrect reference to clause 149.3.2.2.2.

SuggestedRemedy

Change reference to Clause 201.4.2.2.2.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Changed subclause to 201.4.2.2.1.

Cl **201** *SC* **201.4.2.3.1** *P* **104** *L* **1** # 66

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The word "It" lacks clarity

SuggestedRemedy

Replace the word "It" with "PCS receive"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.3.2** *P* **104** *L* **6** # 67

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The in this paragraph is inaccurate and not as good as the wording in Clause 2011.3.2.3.2.

SuggestedRemedy

Replace the whole text in this section with "The descrambling process is as specified in 149.3.2.3.2, except Equation (149-5) shall be applied regardless of whether PHY_S is LEADER or FOLLOWER."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.2.3.3** *P* **104** *L* **23** # 68

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

This is the only mention of R_BLOCK_TYPE in Clause 201. Need reference to where R_BLOCK_TYPE is defined.

SuggestedRemedy

Reference definition of R_BLOCK_TYPE in 149.3.7.2.4

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.4** *P* **104** *L* **39** # 69

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Need to clarify that Equation (149-5) is used regardless of whether PHY_S is LEADER or FOLLOWER

SuggestedRemedy

At the end of the line add "regardless of whether PHY_S is LEADER or FOLLOWER"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.5** *P* **105** *L* **1** # 9

Long, Richard TE Connectivity

Comment Type **E** *Comment Status* **D** *EZ*

Infofield appears to be capitalized everywhere in the document except page 105, line 1 and line 9

SuggestedRemedy

Change "infofield" to "Infofield" and also on line 9

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.5** *P* **105** *L* **5** # 132

van Dyck, Peter Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Clean up Figure 201-15.

SuggestedRemedy

Update Figure 201-15 as follows: Move C/D to the second row and remove first row

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** SC **201.4.5** P **105** L **11** # 130

van Dyck, Peter Infineon

Comment Type **T** *Comment Status* **D** *EZ*

Value of reserved bits in infofield is missing.

SuggestedRemedy

Add the following text at Page 105, Line 11:
"Reserved bits in the infofield represent unused
values and shall be set to zero on transmit and ignored when received by the link partner."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Add the following text at Page 105, Line 11:
"Reserved bits in the Infofield represent unused values and shall be set to zero on transmit and ignored when received by the link partner."

Cl **201** SC **201.4.5** P **105** L **11** # 131

van Dyck, Peter Infineon

Comment Type **T** *Comment Status* **D** *EZ*

Clear definition of infofield transmit bit ordering missing.

SuggestedRemedy

Add the following text at Page 105, Line 11:
"The fields in the infofield are transmitted from left to right starting with C/D. The Block field as well as fields D0 through D6 are transmitted LSB first."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Add the following text at Page 105, Line 11:
"The fields in the Infofield are transmitted from left to right starting with C/D. The Block field as well as fields D0 through D6 are transmitted LSB first."

Cl **201** SC **201.4.5** P **105** L **12** # 180

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

Not all PHY capability fields specified in Clause 149 apply to th LS_PATH PCS

SuggestedRemedy

Change: The message and PHY capability fields are as specified in 149.4.2.4.4 and 149.4.2.4.5.
To: The message and PHY capability fields are as specified in 149.4.2.4.4 and 149.4.2.4.5; however, only Oct10<7> (OAMen) applies.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** SC **201.4.5** P **105** L **16** # 192

Zimmerman, George CME Consulting/ADI,APL Gp, Cisco, Infineon, OnSe

Comment Type **T** *Comment Status* **D** *EZ*

It is unclear whether the setting of OAM bits to 0s during training is an automatic function of the PHY or a requirement on the user. Either way, the existing text is incorrect. I presume it should be an automatic function.

SuggestedRemedy

Change "Note that the OAM (if present) shall be set to all 0s during training." to
"During training, any OAM channel contents (if present) shall be ignored, and zeros transmitted in their place."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Sentence is removed by comment #70.

Cl **201** SC **201.4.5** P **105** L **16** # 129

van Dyck, Peter Infineon

Comment Type **E** *Comment Status* **D** *EZ*

OAM bits shall always be set to 0s during training regardless of OAM being implemented. It currently states "if present".

SuggestedRemedy

Remove "(if present)"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Removed by comment #70.

Cl **201** *SC* **201.4.5** *P* **105** *L* **16** # 70

Jonsson, Ragnar Infineon

Comment Type **T** *Comment Status* **D** *EZ*

There is no need to specify the value of the OAM bits in the training frame, since it should be ignored. Specifying these values can lead to interoperability issues if receiver relies on these values having specific values.

SuggestedRemedy

Remove the sentence "Note that the OAM (if present) shall be set to all 0s during training"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.5** *P* **105** *L* **17** # 133

van Dyck, Peter Infineon

Comment Type **T** *Comment Status* **D** *EZ*

Clarifying text regarding assembly and transmission of training frame is missing.

SuggestedRemedy

Add the following text at Page 105, Line 17, after "Note that the OAM shall be set to all 0s during training.":
"64B/65B blocks of the training frame are processed identical to tx_coded blocks in Clause 201.1.3.2 and the resulting tx_group4x65B block is transmitted as described in Clause 201.4.2.2.14."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.5** *P* **105** *L* **20** # 128

van Dyck, Peter Infineon

Comment Type **E** *Comment Status* **D** *EZ*

In Figure 201-16, extra space "info field"

SuggestedRemedy

Replace with "infofield"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Replace with "Infofield"

Cl **201** *SC* **201.4.5** *P* **105** *L* **26** # 127

van Dyck, Peter Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Remove Infofield table from Figure 201-16, it's already depicted in Figure 201-15

SuggestedRemedy

See comment

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.5** *P* **105** *L* **35** # 30

Tan, Yuxuan Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

Typo

SuggestedRemedy

Change "Scnn[0]" in Equation (201-6) to "Scrn[0]"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.6** *P* **105** *L* **51** # 73

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The wording "As specified" lacks clarity.

SuggestedRemedy

Add at the beginning of the line "Detailed functions and state diagrams are"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.6** *P* **105** *L* **51** # 181

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

The statement doesn't make sense.

SuggestedRemedy

Change: items enclosed in the dotted lines are not present.
To: items enclosed in the dotted lines are not present in the MultiG+100M/100M+MultiGBASE-T1/V1 PHY.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.4.8** *P* **106** *L* **39** # 74

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The wording "As specified" lacks clarity.

SuggestedRemedy

Add at the beginning of the line "The OAM is"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.1** *P* **108** *L* **5** # 265

Fuller, Paul Infineon

Comment Type **T** *Comment Status* **D** *EZ*

100ms should be 50ms

SuggestedRemedy

100ms should be 50ms

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.1** *P* **108** *L* **5** # 75

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The 100 ms is too long, this should only be 50ms.

SuggestedRemedy

Change "100 ms" to "50 ms".

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.2** *P* **108** *L* **8** # 150

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

Combine PMA Transmit function, HS_PATH and PMA Transmit function, LS_PATH into PMA Transmit function. Remove lpi_tx_mode statement which relates to EEE.

SuggestedRemedy

Replace 201.5.2.2 and 201.5.2.3 with new 201.5.2.2: See PMA_Transmit_function.pdf
201.5.2.3.1 becomes 201.5.2.2.1.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

See PMA_Transmit_function_v2.pdf.

Cl **201** *SC* **201.5.2.2** *P* **108** *L* **17** # 76

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

EEE is not supported so reference to ALERT and LPI should be removed.

SuggestedRemedy

Remove the sentence: "When lpi_tx_mode = ALERT, the PN sequence defined in 201.5.2.10 shall be used in place of tx_symb as the data source for PMA Transmit."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Included in #150.

Cl **201** *SC* **201.5.2.2** *P* **108** *L* **17** # 31

Tan, Yuxuan Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

EEE is removed from the document.

SuggestedRemedy

Delete "When lpi_tx_mode=ALERT, the PN sequence defined in 201.5.2.10 shall be used in place of tx_symb as the data source for PMA Transmit."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Included in #150.

Cl **201** *SC* **201.5.2.2** *P* **108** *L* **17** # 118

Lo, William Axonne Inc.

Comment Type **T** *Comment Status* **D** *EZ*

The is no EEE anymore

SuggestedRemedy

Delete the sectence:
When lpi_tx_mode = ALERT, the PN sequence defined in 201.5.2.10 shall be used in place of tx_symb as the data source for PMA Transmit.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Included in #150.

Cl **201** *SC* **201.5.2.2** *P* **108** *L* **23** # 77

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Typo in "FPOLLOWER"

SuggestedRemedy

Replace "FPOLLOWER" with "FOLLOWER"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.2** *P* **108** *L* **23** # 10

Long, Richard TE Connectivity

Comment Type **E** *Comment Status* **D** *EZ*

Typo

SuggestedRemedy

Change "LEADER-FPOLLOWER" to "LEADER-FOLLOWER"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.2** *P* **108** *L* **31** # 207

Zimmerman, George CME Consulting/ADI,APL Gp, Cisco, Infineon, OnSe

Comment Type **T** *Comment Status* **D** *EZ*

"this function" is ambiguous - is it the PMA Transmit Fault function in the previous paragraph?. What appears to be meant is the PMA_transmit_disable variable.

SuggestedRemedy

Replace "this function shall turn off the transmitter" , with "the transmitter shall be turned off"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

See PMA_Transmit_function_v2.pdf.

Cl **201** *SC* **201.5.2.3.1** *P* **109** *L* **10** # 80

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Better to specify behavior based on condition, rather than absence of condition.

SuggestedRemedy

Replace "not SEND_Z" with "SEND_T or SEND_N"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.3.1** *P* **109** *L* **13** # 193

Zimmerman, George — CME Consulting/ADI,APL Gp, Cisco, Infineon, OnSe

Comment Type **E** *Comment Status* **D** *EZ*

There is no 'shall' to do DME encoding, although there are requirements on the definition of what DME encoding is.

SuggestedRemedy

Change "The scrambled data bit An is encoded using Differential Manchester Encoding (DME)." to
"The scrambled data bit, An, shall be encoded using Differential Manchester Encoding (DME) as defined by the following rules:"
Change "shall"s on lines 17, 18, and 19 (clock transitions, data transitions, and otherwise) to "is" (3 occurences)

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.3.1** *P* **109** *L* **13** # 182

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

A shall is needed.

SuggestedRemedy

Change: An, is encoded using Differential Manchester Encoding (DME).
To: An, shall be encoded using Differential Manchester Encoding (DME).

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

See #193

Cl **201** *SC* **201.5.2.3.1** *P* **109** *L* **16** # 81

Jonsson, Ragnar — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Unclear language about "start of each bit", implying to "start of bit period"

SuggestedRemedy

Add the word "period" or the word "symbol" at the end of line 16

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change to: start of each bit symbol

Cl **201** *SC* **201.5.2.3.1** *P* **109** *L* **19** # 82

Jonsson, Ragnar — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Unclear language about "until the next bit", implying to "until the next bit period"

SuggestedRemedy

Add the word "period" or the word "symbol" at the end of line 19

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change to: start of each bit symbol

Cl **201** *SC* **201.5.2.3.1** *P* **109** *L* **45** # 183

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

SuggestedRemedy

Delete Editor's note

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.4** *P* **110** *L* **1** # 151

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

Combine PMA Receive function, HS_PATH and PMA Receive function, LS_PATH into PMA Transmit function.

SuggestedRemedy

Replace 201.5.2.4 and 201.5.2.5 with new 201.5.2.3: See PMA_Receive_function.pdf

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

See PMA_Receive_function_v2.pdf

Cl **201** *SC* **201.5.2.4** *P* **110** *L* **21** # 85

Jonsson, Ragnar Infineon

Comment Type **T** *Comment Status* **D** *EZ*

The polarity swap should be applied to both -T1 and -V1, otherwise we complicate the implementation and loose functionality.

SuggestedRemedy

Remove the words "for -T1".

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

See PMA_Receive_function_v2.pdf

Cl **201** *SC* **201.5.2.5** *P* **110** *L* **29** # 86

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

In accurate or unusual use of the word "comprises", because it is not followed by full "listing" of what comprises the "PMA Receive function"

SuggestedRemedy

Change "comprises" to "includes".

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

See PMA_Receive_function_v2.pdf

Cl **201** *SC* **201.5.2.5** *P* **110** *L* **33** # 32

Tan, Yuxuan Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

Typo

SuggestedRemedy

Change "2*1010" to "2*10-10".

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.5** *P* **110** *L* **33** # 248

Muma, Scott Microchip

Comment Type **E** *Comment Status* **D** *EZ*

Typo in exponent, missing minus sign.

SuggestedRemedy

Change 2e10 to 2e-10 (see P110L8 for similar but correct format).

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.5** *P* **110** *L* **42** # 87

Jonsson, Ragnar Infineon

Comment Type **T** *Comment Status* **D** *EZ*

Since DME is immune to polarity swap, no detection of polarity swap is needed (or even possible).

SuggestedRemedy

Remove the sentence "The receiver uses the sequence of symbols during the training sequence to detect and correct for pair polarity swaps"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.5** *P* **110** *L* **43** # 33

Tan, Yuxuan Motorcomm

Comment Type **E** *Comment Status* **D** *EZ*

Keep consistent with the statement for HS_PATH.

SuggestedRemedy

Change "correct for pair polarity swaps." to "correct for pair polarity swaps for -T1."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

See #87

Cl **201** *SC* **201.5.2.5** *P* **110** *L* **46** # 11

Long, Richard — TE Connectivity

Comment Type **E** *Comment Status* **D** *EZ*

Line 46 looks like an extra line break inserted in the paragraph

SuggestedRemedy

Remove blank line 46

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.6.5** *P* **112** *L* **52** # 92

Jonsson, Ragnar — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The text incorrectly refers to " two optional capabilities", while the current text describes three capabilities: OAMen, PrecodeSel, and InterleaverDepth. However, only the OAMen capability is applicable in the HS direction, the other two do not apply.

SuggestedRemedy

change the text "support of these two optional capabilities" to "support of optional capabilities"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.6.5** *P* **113** *L* **3** # 320

Razavi, Alireza — Infineon

Comment Type **T** *Comment Status* **D** *EZ*

For the high data-rate info-field, interleavedDepth and PrecodedSet should be removed, since they are not defined for the LDR path.

SuggestedRemedy

See comment.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

See #93

Cl **201** *SC* **201.5.2.6.5** *P* **113** *L* **7** # 93

Jonsson, Ragnar — Infineon

Comment Type **T** *Comment Status* **D** *EZ*

Transmitting the "PrecodeSel" and "InterleaverDepth" from PHY_S to PHY_D is meaningless, because these are requesting behaviors that are not supported in the LS_PATH. Therefore they should be removed from the HS_PATH capability list.

SuggestedRemedy

Remove "PrecodeSel" and "InterleaverDepth" from Table 201-7 and the text below the table.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.6.6** *P* **113** *L* **28** # 95

Jonsson, Ragnar — Infineon

Comment Type **T** *Comment Status* **D** *EZ*

The reference to PAM2 to PAM4 transition does not properly describe the transition from SEND_T to SEND_N for 2.5Gbps and 5Gbps.

SuggestedRemedy

Replace "transmitter switches from PAM2 to PAM4" with "transmitter switches from SEND_T to SEND_N"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.6.7** *P* **113** *L* **36** # 96

Jonsson, Ragnar — Infineon

Comment Type **T** *Comment Status* **D** *EZ*

There is no condition defined that would cause "When PMA_state<7:6> is greater than 01" to be true. Therefore, this whole clause is redundant.

SuggestedRemedy

Remove Clause 201.5.2.6.7.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.7** *P* **114** *L* **38** # 97

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

Clause 201.5.2.6.4 is about Message Field, not capability bits.

SuggestedRemedy

Remove reference to 201.5.2.6.4

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.8.2** *P* **116** *L* **6** # 103

Jonsson, Ragnar Infineon

Comment Type **T** *Comment Status* **D** *EZ*

The definition of "infofield_complete" applies equally in both directions.

SuggestedRemedy

Remove the text "for HS_PATH"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Add after HS_PATH: and see 201.5.2.7 for LS_PATH.

Cl **201** *SC* **201.5.2.9** *P* **119** *L* **11** # 110

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The wording "During this period" is ambiguous.

SuggestedRemedy

Change "During this period" to "While in LINK_DOWN state"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.9** *P* **119** *L* **15** # 111

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The wording "As soon as reliable transmission is achieved" lacks clarity

SuggestedRemedy

Change "As soon as reliable transmission is achieved" to "When pcs_data_mode is TRUE"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Changed line to 15.

Cl **201** *SC* **201.5.2.9** *P* **119** *L* **16** # 112

Jonsson, Ragnar Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The text "upon which further PHY operations can take place" is either meaningless or ambiguous.

SuggestedRemedy

Remove the text "upon which further PHY operations can take place"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.10** *P* **120** *L* **4** # 195

Zimmerman, George CME Consulting/ADI,APL Gp, Cisco, Infineon, OnSe

Comment Type **T** *Comment Status* **D** *EZ*

This section describes the link synchronization, but lacks the simple requirement that the state diagram (Figure 201-26) shall be followed.

SuggestedRemedy

Change "shall establish the start of PHY PMA training as defined in 201.5.2.7." to "shall conform to the state diagram in Figure 201-26. This section describes and defines the function of Figure 201-26, the link synchronization process."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.10** *P* **120** *L* **30** # 113

Jonsson, Ragnar — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

The term "SEND_S pusle" is used without any explanation what "SEND_S pulse" means.

SuggestedRemedy

Add the text "The SEND_S signal is a series of pulses." before line 30

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.10** *P* **120** *L* **40** # 197

Zimmerman, George — CME Consulting/ADI,APL Gp, Cisco, Infineon, OnSe

Comment Type **E** *Comment Status* **D** *EZ*

The 5th paragraph here has shalls which duplicate what is in the state diagram. They should be replaced by descriptive text.

SuggestedRemedy

Replace "shall output" at lines 42 and 44 with "outputs"
Replace "shall repeat" at line 45 with "repeats"
Replace "shall stop outputting and enter" at lines 46-47 with "stops outputting and enters"
Replace "shall also enter" at line 48 with "enters"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.5.2.11** *P* **124** *L* **4** # 12

Long, Richard — TE Connectivity

Comment Type **E** *Comment Status* **D** *EZ*

Typo

SuggestedRemedy

Change "LS_PATEH" to "LS_PATH"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.6.1** *P* **124** *L* **34** # 184

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

SuggestedRemedy

Delete Editor's note

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.6.1.1** *P* **126** *L* **17** # 252

Sakunia, Saket — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

replace “...use a reference clock provided by the measurement device” with “...use a reference clock provided by an external clock source”

SuggestedRemedy

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.6.1.1** *P* **126** *L* **34** # 253

Sakunia, Saket — Infineon

Comment Type **E** *Comment Status* **D** *EZ*

replace “...use a reference clock provided by the measurement device” with “...use a reference clock provided by an external clock source”

SuggestedRemedy

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.6.1.1** *P* **127** *L* **35** # 254

Sakunia, Saket Infineon

Comment Type **E** *Comment Status* **D** *EZ*

replace “...use a reference clock provided by the measurement device” with “...use a reference clock provided by an external clock source”

SuggestedRemedy

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.6.1.1** *P* **128** *L* **11** # 255

Sakunia, Saket Infineon

Comment Type **E** *Comment Status* **D** *EZ*

replace “...use a reference clock provided by the measurement device” with “...use a reference clock provided by an external clock source”

SuggestedRemedy

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.6.2** *P* **128** *L* **25** # 3

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting, Microchip, an

Comment Type **E** *Comment Status* **D** *EZ*

Follow IEEE Style

IEEE Editorial Style Manual for Authors
https://journals.ieeeauthorcenter.ieee.org/wp-content/uploads/sites/7/IEEE-Editorial-Style-Manual-for-Authors.pdf
Page 28 - listed in abbreviation list

2021 IEEE SA Standards Style Manual
https://mentor.ieee.org/myproject/Public/mytools/draft/styleman.pdf
Page 29 - used with lowercase in an example

SuggestedRemedy

Replace "DC" with "dc" in three locations:

P128, L25
P136, L32
P227, L35

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.6.2.2** *P* **128** *L* **43** # 201

Zimmerman, George CME Consulting/ADI,APL Gp, Cisco, Infineon, OnSe

Comment Type **E** *Comment Status* **D** *EZ*

149.7.2 only has descriptive text. Saying they "shall be as specified" is inappropriate.

SuggestedRemedy

Change "shall be as specified in 149.7.2." to "are described in 149.7.2."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Corrected to 201.6.2.2, P128/L43

Cl **201** *SC* **201.6.2.4** *P* **130** *L* **34** # 199

Zimmerman, George — CME Consulting/ADI,APL Gp, Cisco, Infineon, OnSe

Comment Type **E** *Comment Status* **D** *EZ*

The way a PSD is measured isn't a requirement on the device, it is a requirement on the user of the standard, which is not appropriate. Measuring a device is different from whether the device is compliant…

The same comment applies to 201.7.2.5 (LS transmitter PSD) as well, on P138

SuggestedRemedy

Change "shall be measured" to "is measured" at P130 L34 and at P138 L18

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.6.2.6** *P* **133** *L* **7** # 138

Johnson, Samuel — Infineon

Comment Type **T** *Comment Status* **D** *EZ*

"short-term" is vague and should be specically defined to a reasonable value

SuggestedRemedy

Change the text to:
"The symbol transmisson rate of the FOLLOWER PHY, when running off of a free-running clock, shall be within the range 5625 x S MHz +1/-20% and, over measuring period of 1ms, frequency variation shall be less than 1% / second

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.6.2.6** *P* **133** *L* **7** # 176

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

According to the IEEE Standards - draft standard template, there should not be a space between a number and the % symbol, e.g. 10%, not 10 %.

SuggestedRemedy

Remove the space betweeen "20" and "%". Remove all spaces between the number and the % symbol.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Val to check 202.

Cl **201** *SC* **201.7.1** *P* **136** *L* **2** # 185

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

Change text to clarify what is sent.

SuggestedRemedy

Change: When test mode 2 is enabled, the PCS shall generate a continuous pattern of 1's.
To: When test mode 2 is enabled, the PHY shall repeatedly transmit DME encoded ones.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.7.2.5** *P* **138** *L* **42** # 186

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **T** *Comment Status* **D** *EZ*

The Figure was updated for the UpperPSD to go to 3500 MHz, but this was not changed in Equation 201-9.

SuggestedRemedy

In Equation 201-9, change 400 to 3500.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.7.2.6** *P* **140** *L* **32** # 187

Wienckowski, Natalie — IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

Change 100M to 100 Mb/s. Also on L36.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.7.2.8** *P* **140** *L* **52** # 200

Zimmerman, George — CME Consulting/ADI,APL Gp, Cisco, Infineon, OnSe

Comment Type **E** *Comment Status* **D** *EZ*

The way rise time is measured isn't a requirement on the device, but an inappropriate requirement on the user. Here though, the measurement point and test mode are part of the definition of the requirement.

SuggestedRemedy

Change "shall be measured" to "is defined as measured"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** *SC* **201.10.1.5** *P* **145** *L* **2** # 13

Long, Richard — TE Connectivity

Comment Type **T** *Comment Status* **D** *EZ*

Use piecewise equation here instead of text

SuggestedRemedy

Remove the limits from the text and place them in a piecewise equation

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Create an equation and a plot of the equation to put in the text.

Cl **201** *SC* **201.10.2.1** *P* **145** *L* **16** # 15

Long, Richard — TE Connectivity

Comment Type **T** *Comment Status* **D** *EZ*

Add PSANEXT formula and explanation text similar to 202.8.2.1

SuggestedRemedy

Copy page 240, lines 6 - 18 and place here, change frequency range to 3 MHz to 4000 MHz in added text.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

P145/L17 Insert the following between the heading and existing text:

The differential pair-to-pair near-end crosstalk (NEXT) loss between the disturbed link segment and the disturbing link segment is specified to meet the bit error ratio objective by limiting the alien crosstalk at the near end of a link segment. Multiple disturber alien NEXT loss is specified as the power sum of the individual alien NEXT disturbers. The power ANEXT loss is derived using Equation (97–25).

Cl **201** *SC* **201.10.2.1** *P* **145** *L* **22** # 14

Long, Richard — TE Connectivity

Comment Type **T** *Comment Status* **D** *EZ*

Error in equation

SuggestedRemedy

Change PSANEXT equation to what is shown on slide 3 of https://www.ieee802.org/3/dm/public/0126/Boyer-Sharma_3dm_01a_0126.pdf (i.e. remove "75" and "80" from the formula)

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change: 6075,6080
To: 60,60

Cl **201** *SC* **201.10.2.2** *P* **146** *L* **1** # 17

Long, Richard — TE Connectivity

Comment Type **T** *Comment Status* **D** *EZ*

Add PSAACRF formula and explanation text similar to 202.8.2.2

SuggestedRemedy

Copy page 241, lines 3 - 21 and place here, change frequency range to 3 MHz to 4000 MHz in added text.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

P146/L4 Insert the following between the heading and existing text:

In order to limit the alien crosstalk at the far-end of a link segment, the differential pair-to-pair alien far-end crosstalk (FEXT) loss between the disturbed link segment and the disturbing link segment is specified to meet the bit error ratio objective. Multiple disturber attenuation to crosstalk ratio far-end ACRF is specified as the power sum of the individual alien ACRF disturbers to limit the total alien FEXT coupled into a link segment. The power AACRF is derived using Equation (97–27).

Cl **201** SC **201.10.2.2** P **146** L **9** # 16

Long, Richard TE Connectivity

Comment Type **T** *Comment Status* **D** *EZ*

Error in equation

SuggestedRemedy

Change PSAACRF equation to what is shown on slide 3 of https://www.ieee802.org/3/dm/public/0126/Boyer-Sharma_3dm_01a_0126.pdf (i.e. remove "75" and "80" from the formula)

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change: 6075,6080
To: 60,60

Cl **201** SC **201.14** P **151** L **1** # 202

Zimmerman, George CME Consulting/ADI,APL Gp, Cisco, Infineon, OnSe

Comment Type **T** *Comment Status* **D** *EZ*

The PHY link includes the medium. The specification is only on the PHY sublayer processing. This impacts both the spec on line 1 (HS_PATH) and line 5 (LS_PATH).

SuggestedRemedy

change "PHY link" to "PHY (local XGMII to remote XGMII link delay minus the link segment propagation delay)" in lines 1 and 5.
Delete the NOTE on line 9 (in its entirety).

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **201** SC **201.14** P **151** L **27** # 156

Wienckowski, Natalie IVN Solutions LLC / Ethernovia

Comment Type **E** *Comment Status* **D** *EZ*

typo

SuggestedRemedy

Change: 100M_MultiGBAE-T1/V1
To: 100M+MultiGBAE-T1/V1

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** SC **202.1.1** P **156** L **20** # 5

Maguire, Valerie Copperopolis; aff'l w/ CME Consulting, Microchip, an

Comment Type **E** *Comment Status* **D** *EZ*

Don't need to have balanced twice in the line for -T1.

The information in parenthesis is "in other words", so "i.e.," should be used.

The double parenthesis bothers the Commenter. :-)

SuggestedRemedy

Replace, "-T1 represents a single shielded balanced pair of conductors (differential (balanced))"

With, "-T1 represents a single shielded balanced pair of conductors (i.e., differential)"

Replace, "-V1 represents a single coaxial cable (single-ended (unbalanced))"

With, "-V1 represents a single-ended coaxial cable (i.e., unbalanced)"

Grant Editor's License to make this same change in other clauses.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** SC **202.1.3** P **158** L **50** # 218

Muma, Scott Microchip

Comment Type **T** *Comment Status* **D** *EZ*

Typically similar clauses have a note indicating the recovered_clock can be used for looptiming.

SuggestedRemedy

Below Figure 202-1 add:
NOTE 1-The recovered_clock arc is shown to indicate delivery of the received clock signal back the PMA TRANSMIT for loop timing.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Hyphen changed to em-dash. Number 1 not needed if only one note. Note can be added below Figure 202-22. too.)

Add at the bottom of Figure 202-1 and Figure 202-22:
"NOTE-The recovered_clock arc is shown to indicate delivery of the received clock signal back the PMA TRANSMIT for loop timing."

Cl **202** *SC* **202.2.1** *P* **162** *L* **39** # 237

Muma, Scott — Microchip

Comment Type **T** *Comment Status* **D** *EZ*

The following primitives were defined, but are not used and no longer needed, so can be removed from this section and from the diagrams.
PMA_TX_TDD_ACTIVE.indication(tx_tdd_active)
PMA_RX_TDD_ACTIVE.indication(rx_tdd_active)
PMA_TX_ON.request(tx_on)
PMA_RX_ON.request(rx_on)

SuggestedRemedy

1. Remove:
PMA_TX_TDD_ACTIVE.indication(tx_tdd_active)
PMA_RX_TDD_ACTIVE.indication(rx_tdd_active)
PMA_TX_ON.request(tx_on)
PMA_RX_ON.request(rx_on)
from the list of primitives.
2. Remove the deleted primitives from all diagrams
3. Delete the subclauses defining these primitives 202.2.1.10, 202.2.1.11, 202.2.1.12, and 202.2.1.13.
4. Delete related variables tx_tdd_active, rx_tdd_active, rx_on, tx_on in sections 202.3.7.2.2 and 202.4.4.1.
5. Remove tx_tdd_active from Figure 202-26
6. Remove the editor's notes that are in the sections being deleted which noted these primitives were TBD/redundant.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.2.1.3** *P* **165** *L* **32** # 272

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

as in Editor's Note: only describes normal operation, not training

SuggestedRemedy

change: "in training mode and in normal operation for all refresh header, 2.5Gb/s mode, and 5Gb/s mode data payloads."

to: "in normal operation for 100M+MultiGBASE-T1/V1, 2.5G+100MBASE-T1/V1, and 5G+100MBASE-T1/V1."

Proposed Response *Response Status* **W**

PROPOSED REJECT.

Commenter withdrew comment.

Cl **202** *SC* **202.2.1.3.1** *P* **165** *L* **28** # 270

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

comma after "tx_symb"

SuggestedRemedy

change "tx_symb the value" to "tx_symb, the value"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Commenter correctly identifies that this sentence reads awkwardly. Improved structure proposed.)

Replace, "During transmission, the PMA_UNITDATA.request simultaneously conveys to the PMA via the parameter tx_symb the value of the symbols to be sent over the MDI."

with, "The PMA_UNITDATA.request primitive conveys the value of the symbol to be transmitted over the MDI via the tx_symb parameter."

Cl **202** *SC* **202.2.1.3.1** *P* **165** *L* **31** # 271

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

wording

SuggestedRemedy

change "10 Gb/s mode's data payload" to "10G+100MBASE-T1/V1"

Proposed Response *Response Status* **W**

PROPOSED REJECT.

Commenter withdrew comment.

Cl **202** *SC* **202.2.1.4.2** *P* **166** *L* **10** # 273

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

The editor's note can be removed as the description had been reviewed and updated during last comment resolution (#142).

SuggestedRemedy

Remove editor's note.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.2.1.7** *P* **167** *L* **29** # 274

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

align with 149.2.2.7

SuggestedRemedy

remove "When the 100M+MultiGBASE-T1/V1 PHY starts Asymmetric training or enters the PCS_TEST state, loc_rcvr_status can be set to NOT_OK."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.2.1.14.2** *P* **172** *L* **6** # 275

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

add text

SuggestedRemedy

Add text: "PMA Receive generates PMA_DET_LP_BURST.indication messages to indicate a change in detect_lp_burst."
Remove editor's note.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: No change to suggested Remedy except to delete double space after "Receive".)

Add text: "PMA Receive generates PMA_DET_LP_BURST.indication messages to indicate a change in detect_lp_burst."
Remove editor's note.

Cl **202** *SC* **202.3.2** *P* **173** *L* **19** # 222

Muma, Scott — Microchip

Comment Type **T** *Comment Status* **D** *EZ*

The rx_oam_field and tx_oam_field don't need to be defined in this diagram as they differ for each PHY. Removing <TBD> in Figure 202-3 is consistent with Figure 202-1.

SuggestedRemedy

In Figure 202-3 delete "<TBD>" in 2 places following rx_oam_field and tx_oam_field.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.2.2.16** *P* **183** *L* **25** # 18

Long, Richard — TE Connectivity

Comment Type **E** *Comment Status* **D** *EZ*

Typo

SuggestedRemedy

Change "Reed-Soloman" to "Reed-Solomon"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.2.2.16** *P* **185** *L* **39** # 277

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

Rows 7 and 8 of Table 202-4 can be removed.

SuggestedRemedy

remove the last two rows of Table 202-4

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.2.2.17** *P* **185** *L* **47** # 244

Muma, Scott — Microchip

Comment Type **T** *Comment Status* **D** *EZ*

The scrambler used in Equation (202-4) should be clarified that it's the PRBS-11.

SuggestedRemedy

Add: DSn[0] in Equation (202-4) is produced using the scrambler defined in 202.3.4.1.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Insert:

"where

DSn[0] is produced using the scrambler defined in 202.3.4.1"

Cl **202** *SC* **202.3.2.2.17** *P* **186** *L* **15** # 241

Muma, Scott — Microchip

Comment Type **E** *Comment Status* **D** *EZ*

202-6 should be a cross-reference to Equation (202-6).

SuggestedRemedy

Format cross-reference appropriately.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.2.2.19** *P* **186** *L* **51** # 278

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

wording

SuggestedRemedy

change "The PCS transmit" to "The 10G+100MBASE-T1/V1 PCS transmit"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change "The PCS transmit" to "The 10G+100MBASE-T1/V1 PCS Transmit"

Editors to globally replace "PCS transmit function" with "PCS Transmit function"

Editors to globally replace "PCS receive function" with "PCS Receive function"

Editors to globally replace "PCS transmit process" with "PCS Transmit process"

Editors to globally replace "PCS receive process" with "PCS Receive process"

Delete Editor's Note on P186, L48

Cl **202** *SC* **202.3.2.3** *P* **188** *L* **29** # 279

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

For "40 consecutive RS-FEC frame errors", there is no difference between LEADER and FOLLOWER.

SuggestedRemedy

remove "(TBD)" and Editor's Note

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.2.3** *P* **190** *L* **40** # 246

Muma, Scott — Microchip

Comment Type **E** *Comment Status* **D** *EZ*

The last symbol (index 511) in the RS-FEC (512 symbols) block of Figure 202-11 should be PAM4 instead of PAM2.

SuggestedRemedy

Change PAM2511 to PAM4511

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.3** *P* **191** *L* **51** # 280

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

Test mode 7 is now described in 202.5.1.

SuggestedRemedy

remove "(TBD)" and Editor's Note

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.2.2** *P* **200** *L* **27** # 281

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

These two sentences can be removed because it is unexpected copied from 202.3.2.3 (line 28 on page 188).

SuggestedRemedy

remove "If 40 (TBD) consecutive RS-FEC frame errors are detected, the block_lock flag is de-asserted. The block_lock flag is re-asserted upon detection of a valid RS-FEC frame"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.2.2** *P* **200** *L* **32** # 282

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

align with 149.3.7.2.2

SuggestedRemedy

remove "(TBD)"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.2.2** *P* **200** *L* **36** # 283

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

lp_low_snr is originally for LPI refresh in 802.3ch. It can be removed.

SuggestedRemedy

remove the variable "lp_low_snr"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.2.2** *P* **200** *L* **40** # 284

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

period is missed

SuggestedRemedy

change "See 202.4.4.1" to "See 202.4.4.1."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.2.2** *P* **201** *L* **27** # 285

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

wording

SuggestedRemedy

change "in user-defined timeout period (usually 3~5 TDD cycles - TBD)." to "in 3 TDD cycles."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.2.3** *P* **202** *L* **7** # 286

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

The timer, Rfer_timer, is redundant.

SuggestedRemedy

remove "Rfer_timer (TBD)"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.2.4** *P* **203** *L* **22** # 287

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

"R_TYPE_NEXT" is used in Figure 202-21.

SuggestedRemedy

remove "(TBD)"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.2.4** *P* **203** *L* **48** # 288

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

802.3ch has this function (T_TYPE_NEXT), but it is not used in any figure.

SuggestedRemedy

remove the function "T_TYPE_NEXT"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Definition and (TBD) needs to be deleted, too.)

remove the function "T_TYPE_NEXT (TBD)" and its defintion on P203, L48-50

Cl **202** *SC* **202.3.7.3** *P* **204** *L* **21** # 219

Muma, Scott — Microchip

Comment Type **T** *Comment Status* **D** *EZ*

The Transmit state diagram is only in Figure 202-20. Figure 202-21 is the Receive state diagram.

SuggestedRemedy

Change: The PCS 64B/65B Transmit state diagram shown in Figure 202-20 and Figure 202-21 controls the
To: The PCS 64B/65B Transmit state diagram shown in Figure 202-20 controls the

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.3** *P* **204** *L* **27** # 220

Muma, Scott — Microchip

Comment Type **T** *Comment Status* **D** *EZ*

Add text explaining the Receive state diagram is in Figure 202-21 and missing from 802.3-2022 p 5999 modified for Clause 202.

SuggestedRemedy

Insert: The PCS 64B/65B Receive state diagram is shown in Figure 202-21 and controls the decoding of 65B received blocks. It makes exactly one transition for each receive block processed.

The PCS shall perform the functions of RFER monitor, Transmit, and Receive as specified in these state diagrams.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.7.3** *P* **207** *L* **30** # 221

Muma, Scott — Microchip

Comment Type **E** *Comment Status* **D** *EZ*

The transition to RX_E from the encircled E is leftover from Clause 149 diagrams related to EEE and should be deleted now.

SuggestedRemedy

Delete the encircled "E" and arrow right above the RX_E state from Figure 202-21.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.3.8.1** *P* **208** *L* **22** # 289

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

Both Rx TDD indication and TxTDD indication are not used and thus can be removed.

SuggestedRemedy

remove "Rx TDD indication" and "TxTDD indication"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Definition and (TBD) needs to be deleted, too.)

remove "Rx TDD indication (TBD)" and "TxTDD indication (TBD)" and their respective definitions on P208, L22-26

Cl **202** *SC* **202.4.1** *P* **209** *L* **43** # 216

Muma, Scott — Microchip

Comment Type **T** *Comment Status* **D** *EZ*

There is a remaining floating line input to Link Monitor and PHY Control that was formerly driven by sync_link_control in Figure 149-26 which should be deleted from this diagram.

SuggestedRemedy

Delete the floating arrows/line below Link Monitor and PHY Control in Figure 202-22. After this change and removal of PMA_LINK.request there are no inputs into the bottom of the Link Monitor and PHY Control blocks.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Clarify that text gets deleted, too. After this change and removal of PMA_LINK.request there are no inputs into the bottom of the Link Monitor and PHY Control blocks.)

Delete the floating arrows/line below Link Monitor and PHY Control and delete the text, "PMA_LINK.request (link_control)" in Figure 202-22.

Cl **202** *SC* **202.4.2.2** *P* **210** *L* **30** # 225

Muma, Scott — Microchip

Comment Type **E** *Comment Status* **D** *EZ*

This sentence is accurate at this point so can remove "(TBD)"

SuggestedRemedy

Delete (TBD)

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.2** *P* **210** *L* **35** # 226

Muma, Scott — Microchip

Comment Type **E** *Comment Status* **D** *EZ*

Update cross reference from 202.x.2 to 202.5.2

SuggestedRemedy

Change 202.x.2 to 202.5.2

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.2** *P* **210** *L* **39** # 227

Muma, Scott — Microchip

Comment Type **E** *Comment Status* **D** *EZ*

Update cross reference from 202.x.2.3 to to 202.5.2.3

SuggestedRemedy

Change 202.x.2.3 to 202.5.2.3

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.2** *P* **210** *L* **41** # 228

Muma, Scott — Microchip

Comment Type **E** *Comment Status* **D** *EZ*

Update cross reference from 202.x.2.3 to to 202.5.2.3

SuggestedRemedy

Change 202.x.2.3 to 202.5.2.3

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.2.1** *P* **210** *L* **46** # 291

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

to align with 802.3bp, 802.3ch and 802.3cy

SuggestedRemedy

remove "(TBD)"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.3** *P* **211** *L* **1** # 260

Gorshe, Steve — Microchip Technology

Comment Type **T** *Comment Status* **D** *EZ*

Use the current value

SuggestedRemedy

Remove the TBD

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Accomodated by Comment #235

Cl **202** *SC* **202.4.2.3** *P* **211** *L* **1** # 235

Muma, Scott — Microchip

Comment Type **T** *Comment Status* **D** *EZ*

It has been confirmed that this is the correct RFER to achieve the target post-FEC BER and FLR, so TBD can be removed.

SuggestedRemedy

Remove (TBD)

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.3** *P* **211** *L* **2** # 236

Muma, Scott — Microchip

Comment Type **T** *Comment Status* **D** *EZ*

This is true for both link segments, so refer to both.

SuggestedRemedy

P211L2: meeting the requirements of 202.7 for -T1 and 202.8 for -V1.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Clarify changed text.)

Replace, "over a channel meeting the requirements of 202.7"

with, "over a -T1 channel meeting the requirements of 202.7 or a -V1 channel meeting the requirements of 202.8"

Cl **202** *SC* **202.4.2.4** *P* **211** *L* **35** # 292

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

wording

SuggestedRemedy

remove "(TBD)"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.4.3** *P* **212** *L* **25** # 293

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

wording

SuggestedRemedy

remove "(TBD)"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.4.3** *P* **212** *L* **30** # 19

Long, Richard — TE Connectivity

Comment Type **E** *Comment Status* **D** *EZ*

Typo

SuggestedRemedy

Burst should not be capitalized, also page 216 line 46

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: "Burst" also appears in two figures.)

Replace "Burst" with "burst" in the following locations:

Figure 202-17
Figure 202-18
P212, L30
P216, L46

Cl **202** *SC* **202.4.2.4.4** *P* **212** *L* **41** # 294

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

wording (many places need to be changed, e.g., lines 41-43 on page 212, Table 202-9 on page 213, etc.)

SuggestedRemedy

change "Training_phase" to "training_phase"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Specify the locations where the change should be made. Missing underscore in Table 202-9 and a state diagram.)

change "Training_phase" to "training_phase" in the following locations:

P212, L41
P212, L42
P212, L43

change "Training phase" to "training_phase" in the header of Table 202-9 and resize columns as necessary.

change "training phase" to "training_phase" on P223, L14

Cl **202** *SC* **202.4.2.4.4** *P* **213** *L* **1** # 295

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

For Table 202-9:
1. Since training_phase<4:3> has 2-bits, its expression should be updated.
2. Since there are total 3 reserved bits, i.e., reserved<2:0>, the last column is redundant.

SuggestedRemedy

1. change "Training_phase" to "training_phase"
2. for the 3rd column: change "0" to "00" and change "1" to "01"
3. remove the 7th column

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Remedy part 1 accomodated by comment #294.)

In Table 202-9:

1. In the 3rd column: change "0" to "00" in three locations and change "1" to "01" in three locatations

2. remove the 7th column

Cl **202** *SC* **202.4.2.4.5** *P* **214** *L* **1** # 296

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

wording: remove "BASE-T1/V1" and add period

SuggestedRemedy

change: "The optional BASE-T1/V1 OAM capability shall be enabled only if both PHYs set the capability bit OAMen=1"
to: "The optional OAM capability shall be enabled only if both PHYs set the capability bit OAMen=1."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.4.5** *P* **214** *L* **4** # 297

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

Precoder is only avilible for 10G mode.

SuggestedRemedy

change: "PrecodeSel indicates the requested precoder."
to: "PrecodeSel indicates the requested precoder, available for 10G only."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Be more specific than 10G, improve grammar,remove conflicting text, and correct minor issues in Table 202-10.)

Replace, "PrecodeSel indicates the requested precoder."

with, "PrecodeSel indicates the requested precoder and is available for 10 Gb/s Speed Capability only (see 202.3.2.2.19).

Replace "2.5G", "5G", and "10G" in Table 202-10 with "2.5 Gb/s", "5 Gb/s", and "10 Gb/s", respectively.

Replace "Negotiated High speed" with "Negotiated speed" on P213, L53

Delete paragraph on P213, L46-48

Cl **202** *SC* **202.4.2.4.6** *P* **214** *L* **37** # 189

Chini, Ahmad — Broadcom

Comment Type **T** *Comment Status* **D** *EZ*

Not a correct statement. delay counter is used in other states as well

Note-The TDD delay_counter fields and PHY capability bits field are only defined during symmetric training
TRAINING0 state, but not defined in other states.
TDD delay counter is only defined during the symmetric training phase, when PMA_state<7:6>=00. The
initial value shall be set to 0.

SuggestedRemedy

remove the Note and the paragraph after that.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.4.7** *P* **215** *L* **12** # 300

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

PhaseSwBC24 has a range (line 22: 16 ~ 256) related to the BC24.

SuggestedRemedy

remove Editor's Note

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.4.7** *P* **215** *L* **26** # 301

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

wording

SuggestedRemedy

remove "(TBD)"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT.

Cl **202** *SC* **202.4.2.4.11** *P* **216** *L* **51** # 302

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

LEADER determins the speed

SuggestedRemedy

change "will be determined (TBD)." to "will be determined by LEADER."

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Missing "the" in Suggested Remedy and on P216, L45.)

change "will be determined (TBD)." to "will be determined by the LEADER."

on P216, L45,

change "payload symbol from LEADER appears" to "payload symbol from the LEADER appears"

Cl **202** *SC* **202.4.4.1** *P* **218** *L* **51** # 217

Muma, Scott — Microchip

Comment Type **T** *Comment Status* **D** *EZ*

PMA_LINK.indication primitive is not defined and should be removed from this description of link_status.

SuggestedRemedy

Change: The link_status parameter set by PMA Link Monitor state diagram and communicated through the
PMA_LINK.indication primitive.
To: The link_status parameter set by PMA Link Monitor state diagram.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change: The link_status parameter set by PMA Link Monitor state diagram and communicated through the PMA_LINK.indication primitive.

To: The link_status parameter set by the Link Monitor state diagram.

Cl **202** *SC* **202.4.4.1** *P* **219** *L* **16** # 305

Wang, Frank — Realtek Semiconductor Corp.

Comment Type **T** *Comment Status* **D** *EZ*

The variable, loc_SNR_margin, is redundant.

SuggestedRemedy

remove "loc_SNR_margin"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Remove variable and definition.)

remove "loc_SNR_margin" and its definition on P219, L16-22

Cl **202** *SC* **202.4.5** *P* **222** *L* **38** # 308

Wang, Frank
Realtek Semiconductor Corp.

Comment Type **E** *Comment Status* **D** *EZ*

wording for Figure 202-26 and 202-27

SuggestedRemedy

1. change "pma_state" to "PMA_state"
2. change "training_phase <= 0" to "training_phase <= 00"
3. change "training_phase <= 1" to "training_phase <= 01"

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: pma_state is also found in Figure 202-27.)

In Figure 202-26, TRAINING0 state:
1. change "pma state" to "PMA_state"
2. change "training_phase <= 0" to "training_phase <= 00"
3. change "training_phase <= 1" to "training_phase <= 01"

In Figure 202-27, change "pma_state" to PMA_state" in three locations.

Cl **202** *SC* **202.5.2.5** *P* **232** *L* **33** # 190

Chini, Ahmad
Broadcom

Comment Type **T** *Comment Status* **D** *EZ*

The specification uses Should for T1 and Shall for V1.

SuggestedRemedy

Use Shall for both T1 and V1.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Suggest grammar can be improved. Commenter meant to use "should" for both -T1 and -V1.)

Replace, "When measured with 100 W termination for -T1 PHYs, the transmit differential signal at the MDI should be less than the peak-to-peak values specified in Table 202-17. When measured with 50 W termination for -V1
PHYs, the transmit signal at the MDI shall be less than the peak-to-peak values specified in Table 202-17."

with, "For -T1 PHYs, the transmit differential signal at the MDI should be less than the peak-to-peak values specified in Table 202-17 when measured with a 100 W termination. For -V1 PHYs, the transmit signal at the MDI should be less than the peak-to-peak values specified in Table 202-17 when measured with a 50 W termination."

where W is the ohms symbol

Cl **202** *SC* **202.12** *P* **247** *L* **13** # 233

Muma, Scott Microchip

Comment Type **E** *Comment Status* **D** *EZ*

The Mode column has the speed in different nomenclature than the rest of the document.

SuggestedRemedy

Editorial license to update Mode to 100M+MultiGBASE, 2.5G+100MBASE, 5G+100MBASE, and 10G+100MBASE nomenclature consistent with other usage in clause 202.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

(Editor's note: Suggest to clarify exact change.)

Replace, "100MBASE-T1/V1" with "100M+MultiGBASE-T1/V1"

Replace, "2.5GBASE-T1/V1" with "2.5G+100MBASE-T1/V1"

Replace, "5GBASE-T1/V1" with "5G+100MBASE-T1/V1"

Replace, "10GBASE-T1/V1" with "10G+100MBASE-T1/V1"