Cl **191** *SC* **191.3.5.2** *P* **104** *L* **16** # 291

Lo, Willliam Axonne Inc.

Comment Type **E** *Comment Status* **X** *late*

ENCODE(tx_raw), EBLOCK_T are underlined and not in same font as other text in Figure 191-19. The C and D labels also need changing.

SuggestedRemedy

Fix the fonts

Proposed Response *Response Status* **O**

Cl **191** *SC* **191.3.5.2** *P* **105** *L* **6** # 293

Lo, Willliam Axonne Inc.

Comment Type **E** *Comment Status* **X** *late*

The arrow into RX_INIT is misaligned and pokes into the box. Also all the arrow heads do not all look like the same size.

SuggestedRemedy

Realign the arrow. Fix arrow head size

Proposed Response *Response Status* **O**

Cl **191** *SC* **191.3.5.2** *P* **105** *L* **18** # 292

Lo, Willliam Axonne Inc.

Comment Type **E** *Comment Status* **X** *late*

DECODE(rx_coded), EBLOCK_R are underlined and not in same font as other text in Figure 191-20. The C, D and E labels also need changing.

SuggestedRemedy

Fix the fonts

Proposed Response *Response Status* **O**

Cl **191** *SC* **191.4.2.2.15** *P* **114** *L* **13** # 285

Lo, Willliam Axonne Inc.

Comment Type **T** *Comment Status* **X** *late*

The CRCout and the diagonal arrow supposed to mean something like a switch. This is a bit confusing and if this is not interpreted as a switch the picutre and the text do not produce the same results.

SuggestedRemedy

See Figure 191-32 for an example on how to fix this.
In line 8 add the sentence:
CRCgen is selected when computing the CRC and CRCout is selected when outputting the 6-bit CRC.

Proposed Response *Response Status* **O**

Cl **191** *SC* **191.5.2.4.5** *P* **125** *L* **14** # 286

Lo, Willliam Axonne Inc.

Comment Type **T** *Comment Status* **X** *late*

The OAMen and VendorSpecificData is not implemented correctly per my previous comment. I think this is a confusion conflating HS_PATH with PHY_S. They are not the same. In this section we are talking about HS_PATH. Explanation:
Let AS be the OAMen advertised bit in PHY_S. Let RD be the bit in PHY_D that reflect the AS bit. In the other direction let AD be the OAMen advertised bit in PHY_D. Let RS be the bit in PHY_S that reflect the AD bit.
Since this is the HS_PATH we need to talk about AS and RD, but instead we are talking about AS and RS here. What may be causing confusion in the text is that we assign 1.2311.1 to both AS and AD and 1.2312.1 to both RD and RS. Note that there is no need to assign the same address to to AS and AD. But since AS and AD are in different devices we chose the same address out of symmetry. Similar for RD and RS.
A similar issue is occurring for VendorSpecificData.

SuggestedRemedy

Change Line 14 from:
The link partner's OAMen is advertised
to:
The value of PHY_S OAMen is reported at the link partner (PHY_D)

Delete Line 19-20 and add:
The VendorSpecificData PHY_S sends to its link partner are bits 1.2316.15:0 (see 45.2.1.249). The value of PHY_S VendorSpecificData is reported at the link partner (PHY_D) in bits 1.2317.15:0 (see 45.2.1.250).

Proposed Response *Response Status* **O**

Cl **191** *SC* **191.5.2.5.4** *P* **127** *L* **36** # 287

Lo, Willliam Axonne Inc.

Comment Type **T** *Comment Status* **X** *late*

Same problem as above but this time we don't even reference the address. As noted in the previous comment AS and AD do not necessarily need to be in the same register address and we cannot infer that, hence we need to state the details even though it may look like we are saying the same thing twice because we chose the same register address to represent AS and AD.

SuggestedRemedy

Line 37:
Remove the entire line and replace with:
OAMen indicates PHY_D OAM capability is enabled. The OAMen is advertised in bit 1.2311.1. (See 45.2.1.244.5 for the definition of bit 1.2311.1.) The value of PHY_D OAMen is reported at the link partner (PHY_S) in bit 1.2312.1. (See 45.2.1.245.4 for the definition of bit 1.2312.1.)

Insert the following at line 42.
The VendorSpecificData PHY_D sends to its link partner are bits 1.2316.15:0 (see 45.2.1.249). The value of PHY_D VendorSpecificData is reported at the link partner (PHY_S) in bits 1.2317.15:0 (see 45.2.1.250).

Delete line 28-29 and add:
The InterleaverDepth request PHY_D sends to its link partner is bits 1.2311.12:11 (see 45.2.1.244.1). The value of PHY_D InterleaverDepth reported at the link partner (PHY_S) in bits 1.2312.12:11 (see 45.2.1.245.1).

Delete line 35 and add:
The value of PHY_D PrecodeSel is reported at the link partner (PHY_S) in bit 1.2312.3:2 (see 45.2.1.245.3).

Proposed Response *Response Status* **O**

Cl **191** *SC* **191.5.2.8** *P* **135** *L* **44** # 288

Lo, Willliam Axonne Inc.

Comment Type **E** *Comment Status* **X** *late*

LEADER TX case incorrect

SuggestedRemedy

Change to Leader TX

Proposed Response *Response Status* **O**

Cl **191** *SC* **191.5.2.8** *P* **138** *L* **35** # 289

Lo, Willliam Axonne Inc.

Comment Type **E** *Comment Status* **X** *late*

Block of text above SYNC_DISABLE wrong font

SuggestedRemedy

Fix font and remove underline.

Proposed Response *Response Status* **O**

Cl **191** *SC* **191.6.2.4** *P* **145** *L* **39** # 290

Lo, Willliam Axonne Inc.

Comment Type **T** *Comment Status* **X** *late*

Does testmode 2 using a free running clock make sense at all? At the high speed transmit rate the Follower high speed Transmitter should never output at a frequency that is +1/-20%. The only time this frequency variation is allowed is during linksync and outputting DME SEND_S pulses.

SuggestedRemedy

Propose we remove the text

Proposed Response *Response Status* **O**